#### **Initial Information Data Sheet**

#### **Inventor Information**

Inventor One Given Name:

Yasuaki

Family Name:

**HIRANO** 

Name Suffix:

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10-42, Naka-machi,

Tenri-shi.

City: State or Province:

Nara-ken, Japan

Postal or Zip Code:

Citizenship Country:

Japan

### **Correspondence Information**

Name Line One:

Barry E. Bretschneider

Name Line Two:

Morrison & Foerster LLP

Address Line One:

2000 Pennsylvania Ave., N.W.

Address Line Two:

City:

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State or Province:

DC

Country:

USA 20006-1888

Postal or Zip Code: Telephone:

202-887-1500

Fax:

202-263-8396

Electronic Mail:

bbretschneider@mofo.com

#### **Application Information**

Title Line One:

NONVOLATILE SEMICONDUCTOR MEMORY DEVICE

Title Line Two:

CAPABLE OF PREVENTING OCCURRENCE OF LUTCHUP

**Total Drawing Sheets:** 

10

Formal Drawings?:

Yes Utility

Application Type: Docket Number:

204552022100

#### **Representative Information**

Representative Customer Number:

25227

## **Continuity Information**

This application is a:

> Application One:

Filing Date:

dc-296030 1

# **Prior Foreign Application**

Foreign Application One: 2001-054270 Filing Date: February 28, 2 February 28, 2001

Country:
Priority Claimed: Japan Yes

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